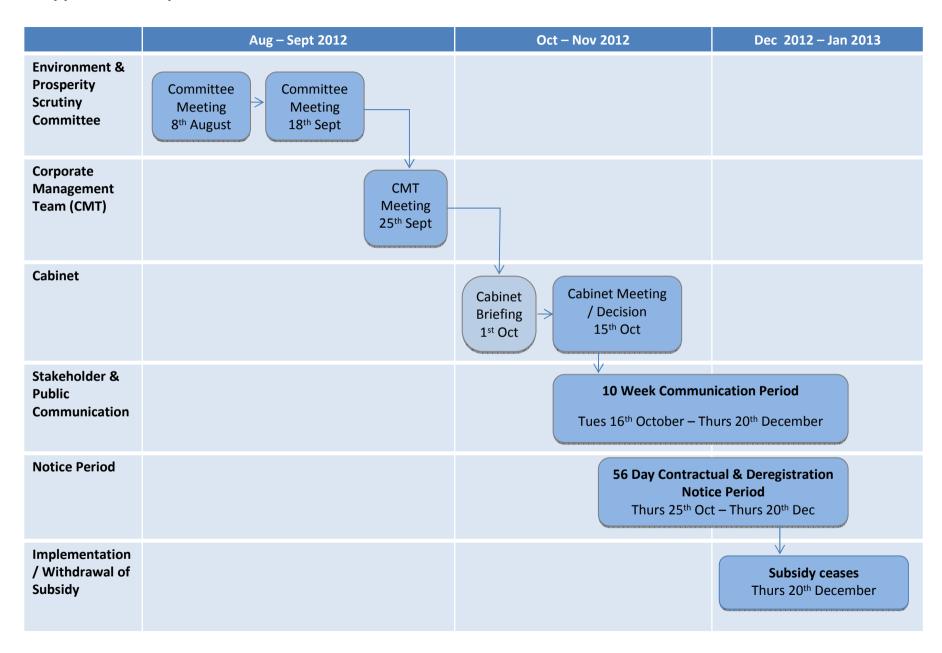
## **Appendix 5 – Implementation Timetable**



This page is intentionally left blank